

providing peripheral line assemblies $[(BG_1...BG_n)]$ that are respectively allocated to one another in pairs and that comprise connections $[(V_1)]$ to one another;

5 mutually monitoring each of said peripheral lines assemblies within each pair of said pairs via said connections; [which a mutual monitoring occurs,]

providing a [at least one] standby circuit assembly $[(BG_E)]$ that takes the place of a [the] down peripheral line assembly in case of a failure of one of said [the] peripheral line assemblies; [(for example, BG_1), as well as]

10 providing [comprising] internal and external interfaces that have an interactive connection to said [the] peripheral line assemblies; $[(BG_1...BG_n)]$ monitoring and controlling all devices with [and comprising] a higher-ranking mechanism; [means (MPSA) that monitors and controls all devices,] [characterized in that]

15 determining an [the] outage of one of said [the] peripheral line assemblies [(for example, BG_1) is determined] by a [the] remaining peripheral line assembly [(for example, BG_2)] that had been paired with said out peripheral line assembly [allocated paired];

20 sending, after said step of determining said outage, a message $[(M_E)]$ is subsequently sent] from said [the] peripheral line assembly [(for example, BG_2)] determining said [the] outage to said [the] standby circuit assembly; $[(BG_E)]$

switching, by said standby circuit assembly, said [whereupon the latter switches the] internal and external interfaces by driving switches; $[(S_1, S_2)]$ and

activating, after said step of switching, said standby circuit assembly by [only then activates] itself.

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2. (Amended) A method [Method] according to claim 1, further comprising the step of [characterized in that the] sending, by said peripheral line assembly [(for example, BG_2)] determining said [the] outage, [additionally sends] an outage message $[(M_A)]$ to said [the] higher-ranking mechanism [means (MPSA)].

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